

10/049792

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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10049792	02/14/2002	349	149	2871	Dung Thai

**\*\*APPLICANTS:** Aoki Hironori;

**\*\*CONTINUING DATA VERIFIED:**  
THIS APPLICATION IS A 371 OF PCT/JP01/04824 06/07/2001

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**\*\*FOREIGN APPLICATIONS VERIFIED:**  
JAPAN 2000-183034 06/19/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		542-007-2
Verified and Acknowledged Examiners's initials		
TITLE : Array substrate and display unit using it and production method for array substrate		
U.S. DEPT. OF COM. / PAT. & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drawg.	Figs. Drawg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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